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# FIG. 1

	0	1	2	3	4	5	6	7
0								
1								
2								
3			×	×	×			
4			×	○0	×			
5			×	○1	×			
6			×	○2	×			
7			×	○3	×			
8			×	×	×			
9								
10								
11								

FIG. 2

×0	×1	×2
×3	○0	△4
×5	△6	△7

FIG. 3

×0 ×3 ×5	×1 ○1 △6	△2 △4 △7

FIG. 4

×0 ×3 ×5	×1 ○2 △6	△2 △4 △7

FIG. 5

×0	×1	△2
×3	○3	△4
△5	△6	△7

FIG. 6

× 0	× 1	× 2	× 3	× 4
× 5	○ 0	○ 4	○ 8	△ 9
× 6	○ 1	○ 5	⊙ 9	△10
× 7	○ 2	○ 6	○10	△11
× 8	○ 3	○ 7	○11	△12
△13	△14	△15	△16	△17

FIG. 7

× 3	× 4	× 5
○ 4	○ 8	△ 9
○ 5	○ 9	△10
○ 6	○10	△11
○ 7	○11	△12
△15	△16	△17

FIG. 8

× 1	× 2	× 3
○ 0	○ 4	○ 8
○ 1	○ 5	○ 9
○ 2	○ 6	○10
○ 3	○ 7	○11
△14	△15	△16

FIG. 9

× 0	× 1	× 3
× 5	○ 0	○ 4
× 6	○ 1	○ 5
× 7	○ 2	○ 6
× 8	○ 3	○ 7
△13	△14	△15

FIG. 10

1	SIGN BIT

0	MSB
0	
0	
0	
0	BIT PLANE TO BE PROCESSED
1	
0	
0	LSB

FIG. 11

| 0 | SIGN BIT |

0	MSB
0	
1	
0	BIT PLANE TO BE PROCESSED
0	
0	
0	
0	LSB

FIG. 12

| 0 | SIGN BIT |

0	MSB
0	
0	
1	BIT PLANE TO BE PROCESSED
0	
0	
0	
0	LSB
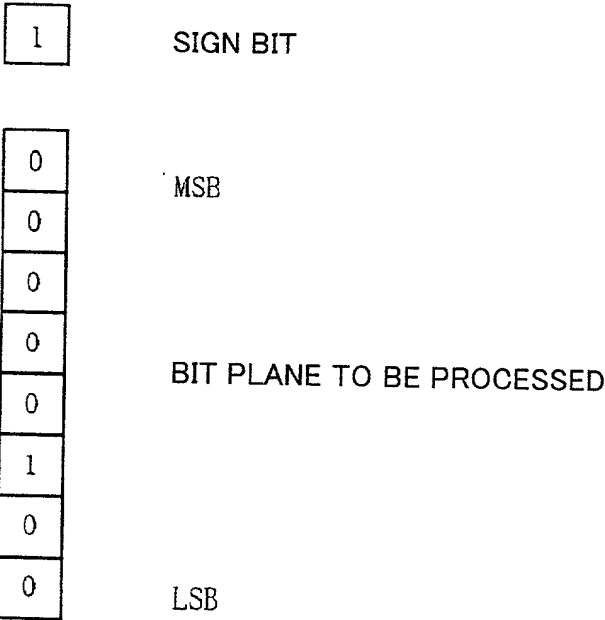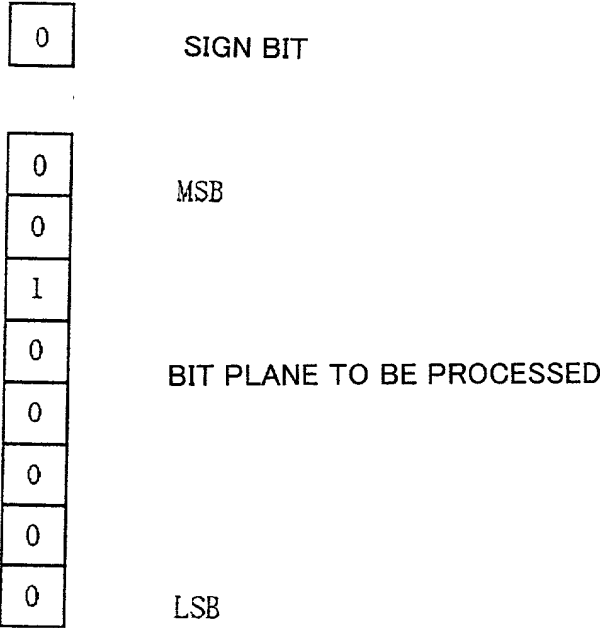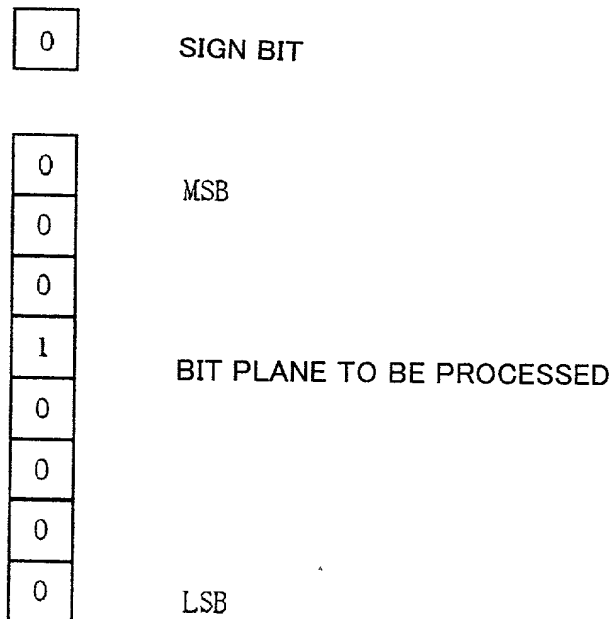
FIG. 13

×000	×001	×002	×003	×004					
×005	○000	○004	○008	∧009					
×006	○001	○005	○009	△010					
×007	○002	○006	○010	△011					
×008	○003	○007	○011	∧012	×100	×101	×102	×103	×104
∧013	∧014	∧015	∧016	△017	×105	○100	○104	○108	∧109
					×106	○101	○105	○109	∧110
					×107	○102	○106	○110	△111
					×108	○103	○107	○111	△112
					△113	△114	△115	△116	△117

FIG. 14

	0	1	2	3	4	5	6	7
0–3	0	1	2	3	4	5	6	7
4–7	8	9	10	11	12	13	14	15
8–11	16	17	18	19	20	21	22	23

FIG. 15

	0	1	2	3	4	5	6	7
0	0	4						
1	1	5						
2	2	6						
3	3	7						
4								
5								
6								
7								
8								
9								
10								
11								